## DECLARATION FOR PATENT APPLICATION

As a below named inventor, I declare: that I verily believe myself to be the original, first and sole (if only one individual inventor is listed below) or an original first and joint inventor (if more than one individual inventor is listed below) of the invention in

## SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD OF TESTING THE SAME

the specification of which is attached hereto unless the following box is checked.

П	was filed on		States Application
_	or PCT Internations	l Application No.	and .
	was amended on	(if app	licable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information of which is material to patalogue to the duty to disclose information of which is material to patalogue to the design of the disclose information of which is material to patalogue to the disclose the d

Country	Category	Application No.	Filing Date	Claim
Japan	Patent	8-136892	May 30, 1996	Yes
Japan	Patent	9-113900	May 1, 1997	Yes

And I hereby appoint Stuart Lubitz (Reg. No. 20, 680). Richard H. Zaitien (Res. No. 27, 248). Alex Chartove (Reg. No. 31, 942). David M. Simon (Res. No. 21, 248). Jay M. Pinkeistein (Res. No. 21, 882). Roger R. Wise (Res. No. 22, 8868). Louis A. Mok (Res. No. 22, 8868). Louis A. Mok (Res. No. 22, 8869). Louis A. Mok (Res. No. 22, 8867). Don P. Scherlacher (Reg. No. 32, 009). Gary D. Mann (Res. No. 34, 867). Don F. Livornese (Reg. No. 32, 240). Ted Rittmaster (Res. No. 38, 229). Paul H. Koveiman (Reg. No. 35, 220). Ted Rittmaster (Res. No. 38, 229). Paul G. Nagy (Reg. No. 77, 39, 260). Joseph F. Vonsauers (Res. No. 38, 690). Gris P. Per (Res. No. 38, 189). David W. Victor (Res. No. 39, 867). Jonathan Y. Kang (Res. No. 38, 189) and Randall C. Furiong (Res. No. 59, 867). Jonathan Y. Kang (Res. No. 38, 189) and Randall C. Furiong (Res. No. 39, 867). Jonathan Y. Kang (Res. No. 38, 189). The State of the Sta

## Roger R. Wise

I declare further that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or prisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may Jeopardize the validity of the application or any patent issued thereon.

## DECLARATION FOR PATENT APPLICATION

I declare further that my post office address is at c/o Intellectual Property Division, KABUSHIKI KAISHA TOSHIBA, 1-1 Shibaura 1-chome, Minato-ku, Tokyo 105, Japan; and that my citizenship and residence are as stated below next to my name:

Inventor: (Signature)	Date	Residence	
Tomomi Momoharo	Date: MAY 23.1997	-	
	Citizen of: Japan	Yokohama-shi, Japan	
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	Date:	-	
	Citizen of: Japan	* .	
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